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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yusuke Igarashi et al. Art Unit : Unknown
Serial No. : 10/824,966 Examiner : Unknown
Filed : April 14, 2004
Title : A METHOD FOR FABRICATING A CIRCUIT DEVICE

Commissioner for Patents
P.O. Box 1450
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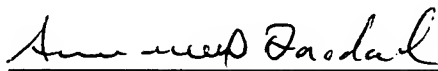
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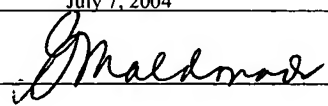
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/824,966	04/14/2004	1765	788	14225-005002	9	21	1

CONFIRMATION NO. 7449

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Applicant(s)

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Kobayashi

Assignment For Published Patent Application

Sanyo Electric Co., Ltd., an Osaka Japan Corporation;

Docketed By	Practice Systems
Action Code:	Correct Filing Receipt
Base Date:	06-28-04
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Initial:	je

Domestic Priority data as claimed by applicant

This application is a CON of 10/172,415 06/14/2002

Foreign Applications

JAPAN P. 2001-185423 06/19/2001

If Required, Foreign Filing License Granted: 06/26/2004

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Early Publication Request: No

Docketed By	Billing Secretary
Due Date:	
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Initials:	

Title

Method for fabricating a circuit device

Preliminary Class

438



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